AUTOMATIC PCB DEFECT DETECTION USING IMAGE PROCESSING ON EMBEDDED PLATFORM

Jani Devanshi Vivekkumar1, Dr. Dipesh Makwana2

1ME Student, Dept of Instrumentation and Control Department, L. D. College of Engineering, Ahmedabad, Gujarat, India
2Associate Professor, Dept of Instrumentation and Control Department, L. D. College of Engineering, Ahmedabad, Gujarat, India

Abstract - Printed Circuit Board is the heart of the electronic automation system because PCB are the most common method of assembling electronic circuits. PCBs are categorized into two types, i.e., bare PCB and the assembled PCB which is commonly known as PCBA. During the manufacturing of PCB, many defects are introduced which are harmful to accurate circuit performance. A PCB Defect detection system uses optical vision which is used to find faults occurred in manual inspection and gives fast, quantitative and dimensional quality. There are many algorithms that have been developed for PCB defect detection, using contact methods or noncontact methods. In Contact method, the connectivity of the electronic circuit is being tested but it is unable to detect major cosmetic defects. Non-contact methods include selection from x-ray imaging, ultrasonic imaging, thermal imaging and optical inspection using image processing. There are three categories of PCB inspection algorithms that's, referential approaches, non-referential approaches, and hybrid approaches. In referential approach, a standard PCB image has to be compared with a PCB image which has to be inspected, and by using a simple subtraction algorithm we can detect the defected regions in the PCB. My focus for this, is to detect defects on printed circuit boards. The results obtained from this method shows that this method can effectively detect common defects on the circuit board, consuming less time than the other practices which makes this system cost effective, fast processing, very accurate and an efficient method to detect the defects. There are two groups in which PCB defects can be categories:

- Functional defects - Functional defects are related to its circuit operation and it can seriously affect the performance of the PCB by causing it to fail.
- Cosmetic defects - Cosmetic defects affect the appearance of the PCB, but it can also threaten its performance in the long run because of the abnormal heat dissipation and distribution of current in the electronic equipment.

A PCB without any placement of electronic components is called Bared PCB. Bare PCB consists of meta coating, patterns, and conductive pathways. The bare PCB must be inspected, in order to reduce cost spending in manufacturing caused by the defected bare PCB. Moganti et al. (1996) proposed three categories of PCB inspection algorithms[22]: (i) Referential approaches consist of image comparison and model-based technique. (ii) Non-referential approaches are based on the verification of the general design rules that is essentially the verification of the widths of conductors and insulators, there is no reference image in this method. Lastly, (iii) hybrid approaches involve a combination of the referential and the non-referential approaches.

These PCB inspection approaches are mainly concentrated on defect detection and to reduce manufacturing costs and time associated with defected bare PCBs. The inspection of bare PCBs is required as the important step of the manufacturing process. However, defect detection did not provide satisfactory information for repairing and quality control work, since the type of detected defects cannot be clearly identified. Based on this incapability of defect detection, defect classification operation is needed in PCB inspection. Therefore, an accurate defect classification procedure is necessary for an

1. INTRODUCTION

A Printed Circuit Board (PCB) is used everywhere from a small toy to a big electronic equipment/machine, which we used in our daily life. PCB consists of an electronic circuit with various electronic components mounted on its surface. During the manufacturing of PCB many defects are introduced which are harmful to accurate circuit performance of that product.
automatic inspection system during PCB production process.

There are 14 types of defects for single layer, bare PCBs[23], and those defects are listed below:

<table>
<thead>
<tr>
<th>No.</th>
<th>Defect name</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Breakout</td>
</tr>
<tr>
<td>2</td>
<td>Pin hole</td>
</tr>
<tr>
<td>3</td>
<td>Open circuit</td>
</tr>
<tr>
<td>4</td>
<td>Under etch</td>
</tr>
<tr>
<td>5</td>
<td>Mouse-bite</td>
</tr>
<tr>
<td>6</td>
<td>Missing conductor</td>
</tr>
<tr>
<td>7</td>
<td>Short circuit</td>
</tr>
<tr>
<td>8</td>
<td>Wrong size hole</td>
</tr>
<tr>
<td>9</td>
<td>Conductor too close</td>
</tr>
<tr>
<td>10</td>
<td>Spurious copper</td>
</tr>
<tr>
<td>11</td>
<td>Excessive short</td>
</tr>
<tr>
<td>12</td>
<td>Missing hole</td>
</tr>
<tr>
<td>13</td>
<td>Over etch</td>
</tr>
<tr>
<td>14</td>
<td>Spur</td>
</tr>
</tbody>
</table>

2. PROPOSED ALGORITHM

2.1 Image Acquisition:

This step means acquiring the image that is to be tested for defects which could be of various image formats such as bitmap (.bmp), portable network graphics (.png), JPEG (.jpg), etc. We can take the same image format or different image format for another image i.e, Reference image of a defect free PCB is taken as input.

2.2 Image Registration:

The reference and test image have different orientation and size, hence images are registered before the image operations. The pixels of both images are mapped accordingly to their similarity of features in the image. For a rotated image the angle of rotation is calculated and after calculating the angle of rotation, the image is then re-rotated and resized for registration.

2.3 Image Complement:

For subtraction and defect detection the image of the PCB which is to be tested is to be complemented. With the help of converting the image to Gray scale and then complementing, the complement of a 3D RGB image has to be done. In Image complement each pixel value of the image is subtracted from the highest value of the image i.e., if it is an 8-bit image maximum value is 28-1 which is equal to 255.

2.4 Image Threshold:

After that the Defects in the images are Threshold according to their size and shape. Image threshold is used to make the pixel below a Threshold value zero i.e., black and pixel having value above the threshold value one i.e., white. By using this operation, it is easier for the appearance of PCB tracks, which is now more visible in an image. The image which is to be tested and the reference image are given same threshold value to reduce complexity of the program.

---

**Fig -1:** Block Diagram of proposed Algorithm
2.5 Image Subtraction:
After image threshold, the next step is image subtraction i.e., subtraction of defective/test image from the reference image. The subtraction is done pixel by pixel of the reference image and the test image. The image registration provides an easy access for image subtraction as a matrix operation.

2.6 Pixel Manipulation:
The defects are seen as white spots in the resultant image, which is caused by image subtraction operation that is performed previously. If no spots exist in the image, it means that the PCB has no defect. If any spots exist in the resultant image, then defects exists in the image.

3. METHODOLOGY

I have used image registration method here. The registration is explained to take place in four basic models which are described as follows:

1. Feature detection:
   Salient and distinctive objects (closed-boundary regions, edges, contours, line intersections, corners, etc.) are manually or, preferably, automatically detected. For further processing, these features can be represented by their point representatives (centers of gravity, line endings, distinctive points), which are called control points (CPs) in the literature.

2. Feature matching:
   In this step, the correspondence between the features detected in the sensed image and those detected in the reference image is established. Various feature descriptors and similarity measures along with spatial relationships among the features are used for that purpose.

3. Transform model estimation:
   The type and parameters of the so-called mapping functions, aligning the sensed image with the reference image, are estimated. The parameters of the mapping functions are computed by means of the established feature correspondence.

4. Image resampling and transformation:
   The sensed image is transformed by means of the mapping functions. Image values in non-integer coordinates are computed by the appropriate interpolation technique.

4. EXPERIMENT AND RESULTS

The Algorithms contain different Image Operations each Image Operation gives out a result. The following chapter focuses on results of the operations done on each part of the project and the final result obtained from these operations.
Fig -4: Matched points inliers and outliers

In Figure 4 The image shows matched points which are used for registration in case of rotated images. The template image is rotated so the points are matched and the angle is recovered which gives us aligned images which help in further operations.

Fig -5: Output of proposed method

The above image is result of image subtraction of the template and defective PCB Image. Second image shows the white spots on the image depict that some defect is present in the Image short, spur and under-etch. And in third image it is converted back to RGB and the boxes have been drawn to have a clear idea of faults in the PCB.

5. CONCLUSION

Printed Circuit Boards are being used everywhere so, it is very important and essential to examine Printed Circuit Boards. Using Image processing techniques and algorithms, examination process has become fast, reliable and effective compared to manual process because it excludes labor intensive job. Automatic PCB defect detection method makes it more advantageous than the manual one. I have presented my implementation of a technique to Detect PCB errors. My technique shows that it is feasible to use the software and detect the errors present in PCB so that further malfunction can be avoided. Its main objective is to detect the errors that are present in PCB during Mass Production. This proposed method can also be used in college labs to detect the errors in PCB which engineering students have to make for their courses. I believe that PCB making or production can be increased efficiently and error rates can be reduced significantly by using this software.

6. REFERENCES


