A Hybrid DC-DC Converter for Standalone Applications

Varun Jo Abu¹, Sija Gopinathan², Leela Salim³

¹PG Scholar, Department of Electrical and Electronics Engineering, Mar Athanasius College of Engineering, Kothamangalam, Kerala, India
²Assistant Professor, Department of Electrical and Electronics Engineering, Mar Athanasius College of Engineering, Kothamangalam, Kerala, India
³Assistant Professor, Department of Electrical and Electronics Engineering, Mar Athanasius College of Engineering, Kothamangalam, Kerala, India

Abstract - The conventional energy sources available in the world are diminishing every year creating a huge difference between the demanded energy and the available energy. Efficient utilization of the ample renewable energy resources present in the world can be used to cover this extra demand. With the use of this renewable energy sources the levels of pollution and global warming can controlled to a lower level. A significant demerit with the renewable energy sources is their fluctuating nature. Hybridization technique is hence utilized to overcome this problem. This allows two sources to meet the load separately or simultaneously depending on the availability. In this paper a double input single output DC-DC converter is presented for stand-alone systems. The topology operates as a bootstrap circuit by employing the charging switches in case of only one input port powering and one input being short circuited and maintains the expected output voltage making the system fault tolerant.

Key Words: Hybrid system, Multi-input converter, Bootstrap operation, Solar panel, Fuel cell

1. INTRODUCTION

Rise in the energy demand and limitation in fossil fuels have increased the role of renewable energy resources. Renewable energy sources will have to play a significant role in overcoming these problems. Systems based on petroleum product and fossil fuels will result in environmental pollution and hence global warming. This elevated the need for renewable energy sources which is abundant and environment friendly. Solar energy, wind energy, etc. are capable of supplying energy to meet the power demand. Even though the capital cost and space requirement for such systems are high, the running cost is extremely low. They can be easily utilized in distributed generation, micro grid, stand-alone systems, rural telephony systems etc. The main disadvantage of such a system is its unpredictable and intermittent nature, i.e., the output from these systems will always be fluctuating and doesn’t deliver a constant output. To overcome this challenge, hybrid renewable energy systems were implemented which clubs two or more energy resources to produce a constant output. Hybridization improves the efficiency and life of the system and also brings down the storage requirement. However, by combining these two fluctuating sources, the efficiency and reliability of the system can be refined notably. At the same time, the capital cost and complexity can increase as a result of hybridization.

Multi input converters (MIC) have a simpler structure, improved power density and lower cost due to sharing of switches and other components [1]-[3]. Dynamic performance can be improved and complex communication among multiple different sources can be avoided due to the unified power management with centralized control. Thus MIC is appropriate for renewable power systems. Isolated [4]-[6] and non-isolated MIC has been developed for DC-DC conversion. Galvanic isolation is used in isolated MICs, but the disadvantage is the larger number of active switches present in it. Non-isolated converters have the advantages of small size, high power density and more efficiency. Based on buck and/or boost converter structures, various non-isolated topologies were developed. A triple input boost DC-DC converter developed in [7] consists of a photovoltaic source, fuel cell and a battery in a unified structure. A buck voltage source cell parallel connected MIC is presented in [8] which can operate in buck, boost or buck-boost mode, but with only one of the input ports powering. Two-input series-connected buck DC-DC converters proposed in [9]-[11] have a simple topology and efficient energy utilization. They can deliver power to the load from two power sources simultaneously or individually. The series-connected two-input converter in [10] has one input port connected to a renewable energy source and other to a storage element, used for nanogrid application. Although both individual and simultaneous operations are possible for this converter, when one input source is shut down, the other port takes up the operation and the output voltage will be less than the existing input voltage. This implies that the converter is not able to maintain the expected output voltage in the one port powering case if the expected output voltage is more than either of two input voltages.

Due to the randomness and intermittency of renewable energy sources present as inputs, it is necessary for the converter to be loaded with a flexible compensation program for error free individual operation. A circuit topology was designed in [12] which include a combination of a charging switch and a series connected double input converter (SCDIC). In this circuit, the converter operates in bootstrap mode in the case of one port powering and it meets the expected output voltage. The disadvantage with this topology...
is that it employs a large number of switches and hence the switching losses will be larger. But this topology has the merit of employing only a single inductor for any number of input ports when compared to parallel boost converters. This advantage will in turn reduce the volume and cost and improve the dynamic response of the system.

This paper proposes another series connected double input converter with reduced number of switches and possessing all the advantage of the circuit presented in [12]. With the presence of a blocking diode, the chance of fault due to a short circuited input can be ruled out. In such a case, the short circuit input port will be separated from the converter and the port will be powered by bootstrap operation.

The paper consists of four sections. After introduction in section I, the circuit features, principle and modes of operation will be described in section II. Section III presents the simulation model and results of the converter and section IV concludes the paper.

### Table -1: Comparison of the proposed converter with SCDIC converter [12]

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SCDIC CONVERTER</th>
<th>PROPOSED CONVERTER</th>
</tr>
</thead>
<tbody>
<tr>
<td>No. of inputs</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>No. of switches</td>
<td>6</td>
<td>5</td>
</tr>
<tr>
<td>No. of diodes</td>
<td>1</td>
<td>2</td>
</tr>
</tbody>
</table>

### 2. HYBRID DC-DC CONVERTER

A circuit diagram of the hybrid energy system is shown in figure 3, where one of the inputs is connected to the PV array and the other input is connected to a fuel cell. \( V_{in1} \) and \( V_{in2} \) are the two input source voltages. \( C_1 \) is the bootstrap capacitor and is connected across \( V_{in2} \). \( D_1 \) is the breaking diode and it separates a short circuited input source from the converter. Switches \( S_1 \) and \( S_3 \) are the charging switches during bootstrap operation. \( L_f \) is the filter inductor, \( C_f \) is the filter capacitor and \( R_o \) is the load resistor. All the switches in the converter are MOSFETs. This configuration allows both individual and simultaneous operation of the converters. So the circuit can operate in three modes depending on the available inputs. The circuit has 6 states of operation. When both sources are unavailable the entire circuit will be turned off. Table 2 shows the modes of operation.

### Table -2: Modes of operation

<table>
<thead>
<tr>
<th>MODE</th>
<th>( V_{in1} ) (SOLAR SOURCE)</th>
<th>( V_{in2} ) (FUEL CELL)</th>
</tr>
</thead>
<tbody>
<tr>
<td>I (Simultaneous operation)</td>
<td>ON</td>
<td>ON</td>
</tr>
<tr>
<td>II (Bootstrap operation)</td>
<td>OFF</td>
<td>ON</td>
</tr>
<tr>
<td>III</td>
<td>ON</td>
<td>OFF</td>
</tr>
</tbody>
</table>

![Fig -1: Circuit diagram of the hybrid DC-DC converter for standalone applications](image1)

Although modes I and II can occur in a practical situation, mode III has least chances to occur. Figure 1 shows the circuit diagram of the converter. States 1 and 2 is during the bootstrap operation mode, i.e., during the individual operation of \( V_{in2} \). Rest of the states from 3-6 occurs at the remaining modes, i.e., during individual operation of \( V_{in1} \) or during simultaneous operation. The expected output voltage is kept above either of the two input voltages. Take \( V_{in1} > V_o > V_{in2} \).

![Fig -2: Equivalent circuit during (a) State I, (b) State II](image2)

**State I:** (\( S_2, S_4, S_5 \) ON, \( S_1, S_3 \) OFF)

\( V_{in2} \) charges the capacitor \( C_f \) through the switches \( S_2, S_4 \) and \( S_5 \). Now the voltage across the points A and B (\( V_{AB} \)) will be \( V_{in2} \) and it is below \( V_o \). Now the voltage appearing across the inductor \( L_f \) is negative and thereby the current \( i_{L_f} \) decreases. The power to load is supplied alone by \( V_{in2} \) in this state. Figure 2(a) shows the equivalent circuit during state I.

\[
\frac{dv_{AB}}{dt} = V_{in2} \tag{1}
\]

\[
\frac{dL_f}{dt} = \frac{V_{in2} - V_o}{L_f} \tag{2}
\]

**State II:** (\( S_1 \) ON, \( S_2, S_3 S_4, S_5 \) OFF)

\( V_{in2} \) is now in series with the bootstrap capacitor \( C_f \). Now the voltage across the points A and B (\( V_{AB} \)) will be 2\( V_{in2} \) and it is above \( V_o \). This makes the voltage across the inductor \( L_f \) positive and thereby the current \( i_{L_f} \) rises. Figure 2(b) shows the equivalent circuit during state II.

\[
v_{AB} = 2V_{in2} \tag{3}
\]
It can be seen that when \( V_{in1} \) is powered off from its port, the port is taken up by \( V_{in2} \) through bootstrap operation and the expected output voltage is met. For the remaining states, both sources \( V_{in1} \) and \( V_{in2} \) are available and simultaneous operation of the converter takes place.

**State III:** \((S_1 \text{ON}, S_2, S_3, S_4, S_5 \text{OFF})\)

Sources \( V_{in1} \) and \( V_{in2} \) connected in series during this state to deliver the power to the load. The voltage across the points \( A \) and \( B \) \( (V_{AB}) \) will be \( V_{in1} + V_{in2} \) and it is more than \( V_o \). Now the voltage appearing across the inductor \( L_f \) is \((V_{in1}+V_{in2}-V_o)\) and thereby the current \( i_{Lf} \) increases linearly. Figure 3(a) shows the equivalent circuit during state III.

\[
\frac{di_{Lf}}{dt} = \frac{2(V_{in1}-V_o)}{L_f} \quad (4)
\]

\[
v_{AB} = V_{in1} + V_{in2} \quad (5)
\]

\[
\frac{di_{Lf}}{dt} = \frac{V_{in1}+V_{in2}-V_o}{L_f} \quad (6)
\]

**State IV:** \((S_4, S_5 \text{ON}, S_1, S_2, S_3 \text{OFF})\)

Input source \( V_{in1} \) only deliver the power to the load. The voltage across the points \( A \) and \( B \) will be \( V_{in1} \) and it is more than \( V_o \). Now the voltage appearing across the inductor \( L_f \) is \((V_{in1}+V_o)\) and thereby the current \( i_{Lf} \) increases. Figure 3(b) shows the equivalent circuit during state IV.

\[
\frac{di_{Lf}}{dt} = \frac{V_{in1}-V_o}{L_f} \quad (7)
\]

**State V:** \((S_2 \text{ON}, S_1, S_3, S_4, S_5 \text{OFF})\)

Input source \( V_{in2} \) only supplies the power to the load. The voltage across the points \( A \) and \( B \) will be \( V_{in2} \) and it is less than \( V_o \). Now the voltage appearing across the inductor \( L_f \) is \((V_{in2}+V_o)\) and thereby the current \( i_{Lf} \) reduces. Figure 4(a) shows the equivalent circuit during state V.

\[
\frac{di_{Lf}}{dt} = \frac{V_{in2}-V_o}{L_f} \quad (8)
\]

\[
v_{AB} = 0 \quad (9)
\]

\[
\frac{di_{Lf}}{dt} = -\frac{V_o}{L_f} \quad (10)
\]

Depending on the availability of the sources, their input voltages and power, the order of states during simultaneous operation will be states III, IV, VI or states III, V, VI. If more power is to be taken from the input source 1, state IV follows state III. In case more power is to be withdrawn from input source 2, state V follows state VI. Figure 5(a) & (b) shows the waveforms related to mode I (simultaneous operation). Figure 6 presents the corresponding waveforms of mode II (bootstrap operation/individual operation).
3. SIMULATION MODELS AND RESULTS

The simulations were done on MATLAB/ Simulink R2014a. Figure 12 shows the simulink model of the converter.

Table 3: Simulation components, parameters and values

<table>
<thead>
<tr>
<th>COMPONENT AND PARAMETERS</th>
<th>VALUE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage 1 ($V_{in1}$)</td>
<td>50 V</td>
</tr>
<tr>
<td>Input Voltage 2 ($V_{in2}$)</td>
<td>30 V</td>
</tr>
<tr>
<td>Output Voltage ($V_o$)</td>
<td>40 V</td>
</tr>
<tr>
<td>Bootstrap Capacitor ($C_1$)</td>
<td>220 μF</td>
</tr>
<tr>
<td>Filter Inductance ($L_f$)</td>
<td>400 μH</td>
</tr>
<tr>
<td>Filter Capacitance ($C_f$)</td>
<td>300 μF</td>
</tr>
<tr>
<td>Load Resistance ($R_o$)</td>
<td>2.2 kΩ</td>
</tr>
<tr>
<td>Switching Frequency ($f_s$)</td>
<td>50 kHz</td>
</tr>
</tbody>
</table>

Figure 7 shows the output voltage and current during the simultaneous operation, i.e., during mode I. The output voltage settles at 40 V.

Figure 8 shows the gate pulses to the converter switches during mode I. The voltage stresses across each of the switches are plotted in figure 9. The maximum voltage stress across a switch is 80 V. Figure 10 shows the ripples in the output voltage waveform. Ripple percentage is seen as 1.51% of the average value.

Figure 11 shows the output voltage and current during the bootstrap operation, i.e., during mode II. Although $V_{in2}$ is only available, the output voltage is obtained as 40 V.

Figure 13 shows the gate pulses to the converter switches during mode I. The voltage stresses across the switches are plotted in figure 14. The maximum voltage stress across a switch is 50 V during this mode. Figure 15 shows the ripples in the output voltage waveform. Output voltage ripple is obtained as 0.042%.
4. CONCLUSIONS

The hybrid DC-DC converter for standalone applications is a simple and robust topology to solve the problems related to renewable energy source and ensure a constant output voltage. With the help of bootstrap operation, the fuel cell port can charge the renewable energy port if the renewable energy source is powered off from the circuit. This circuit contains only a single inductor and there is no need for extra inductors while adding more input ports. This reduces the size and cost of the converter. Power density is improved and a good dynamic response is possible with this circuit. It is highly efficient during simultaneous and individual operation of the converter.

REFERENCES


