

CLOSED LOOP ANALYSIS OF SPACE VECTOR MODULATED MATRIX CONVERTER USING PI AND FUZZY LOGIC CONTROLLERS

Smitha M¹, H.V.Govindaraju²

¹M.Tech Scholar, Power Electronics, Dr. Ambedkar Institute of Technology, Bengaluru, Karnataka, India- 560056

²Associate Professor, Department of Electrical and Electronics Engineering, Dr. Ambedkar Institute of Technology, Bengaluru, Karnataka, India- 560056

Abstract: A matrix converter performs direct transfer of electrical energy from input to output because of which the distortion and unbalance of the input voltage will greatly affect the output of the MC. In order to reduce the distortion in the matrix converter, a closed loop control of MC has been presented in this paper. A comparison in the operation of matrix converter with PI and fuzzy logic controller in the feedback individually has been performed. A fuzzy logic controller is an automatic controller that controls an object in accordance with the desired behaviour. The control action of fuzzy logic controllers is based on "if-then rules". These set of rules describe the system behaviour. The closed loop analysis has been performed for both balanced as well as unbalanced grid conditions. Space vector modulation technique has been employed to control the matrix converter. Performance of the closed loop control of the matrix converter is analysed using MatLab / Simulink.

Key Words: Matrix converter (MC), space vector modulation, PI controller, Fuzzy Logic Controller (FLC).

1.INTRODUCTION

The most interesting and promising member in the power electronic converter family is MC that provides direct AC-AC power conversion .The growth of power device technology and power ICs research work on exploring MC has increased. Matrix converter (MC) is a converter with three input and three output phases consisting of nine bidirectional switches distributed in a 3X3 matrix form. This topology provides direct AC-AC energy conversion. In contrast to conventional ac/ac converters, MC does not require an intermediate stage of DC conversion. It eliminates the need of DC link reactive component but requires small ac filters for the removal of switching ripples and also provides bidirectional power flow. One of the most important features of MC is that it is capable of converting magnitude and frequency of the input into a desired magnitude and frequency of the output using power semiconductor switches. The MC can be used to replace the

conventional AC-DC-AC system which employs two stage power conversions.

A $3_{\phi}-3_{\phi}$ MC consists of a bidirectional switch arranged in a matrix form. Such an arrangement provides the convenience in connecting any input phase to any output phase at any time. Fig- (1) shows the topology of matrix converter.

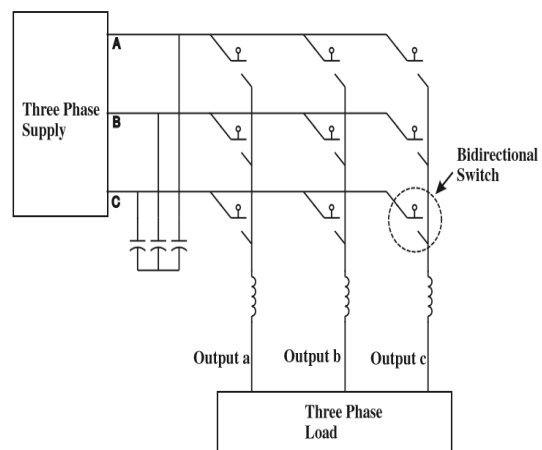


Fig-(1):Matrix converter topology.

A space vector modulation technique has been extensively used nowadays around the globe. This is because of the fact that it provides increased voltage gain & reduces the harmonic distortion. SVM applied to MC is of two types- Direct & indirect SVM. Even though application of SVM for an MC is a tedious method, it provides better output compared to other modulation techniques.

2. FUNDAMENTALS OF MATRIX CONVERTER

The switching of switch of MC can be defined as

$$S_{kj}(t) = \begin{cases} 1, & \text{switch closed} \\ 0, & \text{switch open} \end{cases}$$

Where $k,j=\{1,2,3\}$

Also $S_{K1}+S_{K2}+S_{K3}=1$ and $k=\{1,2,3\}$ Eqn (1)

The relation between the input voltages V_a, V_b, V_c and the output voltages V_A, V_B, V_C can be obtained as ,

$$\begin{bmatrix} v_a(t) \\ v_b(t) \\ v_c(t) \end{bmatrix} = \begin{bmatrix} S_{Aa}(t) & S_{Ba}(t) & S_{Ca}(t) \\ S_{Ab}(t) & S_{Bb}(t) & S_{Cb}(t) \\ S_{Ac}(t) & S_{Bc}(t) & S_{Cc}(t) \end{bmatrix} \begin{bmatrix} v_A(t) \\ v_B(t) \\ v_C(t) \end{bmatrix} \text{..Eqn (2)}$$

Similarly, for a balanced load the relation between the input and output currents can be obtained as,

$$\begin{bmatrix} i_A(t) \\ i_B(t) \\ i_C(t) \end{bmatrix} = \begin{bmatrix} S_{Aa}(t) & S_{Ab}(t) & S_{Ac}(t) \\ S_{Ba}(t) & S_{Ba}(t) & S_{Bc}(t) \\ S_{Ca}(t) & S_{Cb}(t) & S_{Cc}(t) \end{bmatrix} \begin{bmatrix} i_a(t) \\ i_b(t) \\ i_c(t) \end{bmatrix} \text{..Eqn (3)}$$

Where S_{Aa} through S_{Cc} are switching variables.

In a $3\phi-3\phi$ mc, we have bidirectional switches owing to $2^9=512$ possible stage. Because of some constraints all of them cannot be used. Only 27 switch combinations as shown in table 1 can be employed. These 27 switching combinations can be divided into 3 groups.

3.SPACE VECTOR MODULATION

Space vector modulation is a type of PWM technique that treats the sinusoidal voltage as a phase or amplitude that rotates at frequency ω . This vector is represented in d-q reference frame. The vector summation of three modulating signals is known as reference voltage.

For a balanced three phase sinusoidal system,

$$\begin{bmatrix} V_u(t) \\ V_v(t) \\ V_w(t) \end{bmatrix} = V_o \begin{bmatrix} \cos \omega_0 t \\ \cos(\omega_0 t - 120^\circ) \\ \cos(\omega_0 t - 240^\circ) \end{bmatrix} \text{.....Eqn (4)}$$

In terms of complex space vector ,

$$\vec{V}_o = \frac{2}{3} [V_u(t) + V_v(t)e^{j\frac{2\pi}{3}} + V_w(t)e^{j\frac{4\pi}{3}}] = V_o e^{j\omega_0 t} \text{Eqn (5)}$$

$2/3$ is scaling factor.

The indirect transfer approach has been employed. In this approach the operation of MC is considered to be equivalent to that of (Voltage Source Rectifier) VSR and (Voltage Source Inverter) VSI. Hence VSR-SVM and VSI-SVM are reviewed in this section.

Table -1: Switching table

Group	ON Switch	V_u	V_v	V_w	I_a	I_b	I_c	V_o	ω_0	I_a	ω_a		
I	S_{uu}	S_{vv}	S_{ww}	V_u	0	$-V_u$	$-I_a$	0	$2/\sqrt{3}V_u$	0	$2/\sqrt{3}I_a$	$-\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	$-V_u$	0	V_u	I_a	0	$-2/\sqrt{3}V_u$	0	$-2/\sqrt{3}I_a$	$-\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	V_u	0	$-V_u$	0	I_a	$2/\sqrt{3}V_u$	0	$2/\sqrt{3}I_a$	$\pi/2$	
	S_{uu}	S_{vv}	S_{ww}	$-V_u$	0	V_u	0	$-I_a$	$-2/\sqrt{3}V_u$	0	$-2/\sqrt{3}I_a$	$\pi/2$	
	S_{uu}	S_{vv}	S_{ww}	V_u	0	$-V_u$	$-I_a$	0	$2/\sqrt{3}V_u$	0	$2/\sqrt{3}I_a$	$7\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	$-V_u$	0	V_u	I_a	0	$-2/\sqrt{3}V_u$	0	$-2/\sqrt{3}I_a$	$7\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	$-V_u$	V_u	0	I_a	$-I_a$	0	$2/\sqrt{3}V_u$	$2\pi/3$	$-\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	V_u	$-V_u$	0	$-I_a$	I_a	0	$-2/\sqrt{3}V_u$	$2\pi/3$	$-\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	$-V_u$	V_u	0	0	I_a	$2/\sqrt{3}V_u$	$2\pi/3$	$2/\sqrt{3}I_a$	$\pi/2$	
	S_{uu}	S_{vv}	S_{ww}	V_u	$-V_u$	0	0	$-I_a$	$-2/\sqrt{3}V_u$	$2\pi/3$	$-2/\sqrt{3}I_a$	$\pi/2$	
	S_{uu}	S_{vv}	S_{ww}	$-V_u$	V_u	0	$-I_a$	0	$2/\sqrt{3}V_u$	$2\pi/3$	$2/\sqrt{3}I_a$	$7\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	V_u	$-V_u$	0	I_a	0	$-2/\sqrt{3}V_u$	$2\pi/3$	$-2/\sqrt{3}I_a$	$7\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	0	$-V_u$	V_u	I_a	$-I_a$	0	$2/\sqrt{3}V_u$	$4\pi/3$	$-\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	0	V_u	$-V_u$	$-I_a$	I_a	0	$-2/\sqrt{3}V_u$	$4\pi/3$	$-\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	0	$-V_u$	V_u	0	I_a	$-I_a$	$2/\sqrt{3}V_u$	$4\pi/3$	$\pi/2$	
	S_{uu}	S_{vv}	S_{ww}	0	V_u	$-V_u$	0	$-I_a$	I_a	$-2/\sqrt{3}V_u$	$4\pi/3$	$\pi/2$	
	S_{uu}	S_{vv}	S_{ww}	0	$-V_u$	V_u	$-I_a$	0	I_a	$2/\sqrt{3}V_u$	$4\pi/3$	$7\pi/6$	
	S_{uu}	S_{vv}	S_{ww}	0	V_u	$-V_u$	I_a	0	$-I_a$	$-2/\sqrt{3}V_u$	$4\pi/3$	$7\pi/6$	
	II	S_{uu}	S_{vv}	S_{ww}	0	0	0	0	0	0	0	0	
		S_{uu}	S_{vv}	S_{ww}	0	0	0	0	0	0	0	0	
		S_{uu}	S_{vv}	S_{ww}	0	0	0	0	0	0	0	0	
		S_{uu}	S_{vv}	S_{ww}	V_u	V_u	V_u	I_a	I_a	I_a	$\omega_0 t$	I_a	$\omega_0 t$
		S_{uu}	S_{vv}	S_{ww}	$-V_u$	$-V_u$	$-V_u$	I_a	I_a	I_a	$-\omega_0 t$	I_a	$-\omega_0 t$
		S_{uu}	S_{vv}	S_{ww}	$-V_u$	$-V_u$	$-V_u$	I_a	I_a	I_a	$\omega_0 t + 4\pi/3$	I_a	$\omega_0 t + 2\pi/3$
	III	S_{uu}	S_{vv}	S_{ww}	V_u	V_u	V_u	I_a	I_a	I_a	$\omega_0 t$	I_a	$\omega_0 t$
		S_{uu}	S_{vv}	S_{ww}	$-V_u$	$-V_u$	$-V_u$	I_a	I_a	I_a	$-\omega_0 t$	I_a	$-\omega_0 t$
		S_{uu}	S_{vv}	S_{ww}	V_u	V_u	V_u	I_a	I_a	I_a	$\omega_0 t + 4\pi/3$	I_a	$\omega_0 t + 2\pi/3$
S_{uu}		S_{vv}	S_{ww}	$-V_u$	$-V_u$	$-V_u$	I_a	I_a	I_a	$-\omega_0 t$	I_a	$-\omega_0 t$	
S_{uu}		S_{vv}	S_{ww}	V_u	V_u	V_u	I_a	I_a	I_a	$\omega_0 t + 2\pi/3$	I_a	$\omega_0 t + 4\pi/3$	
S_{uu}		S_{vv}	S_{ww}	$-V_u$	$-V_u$	$-V_u$	I_a	I_a	I_a	$-\omega_0 t$	I_a	$-\omega_0 t$	

a) VSR-SVM

The reference current space vector is approximated using adjacent vectors. The input current vector and vector diagrams are shown in Fig-(2, 3). The duty cycles are given by,

$$d_{\alpha i} = m_i * \sin(\pi / 3 - \theta_i)$$

$$d_{\beta i} = m_i * \sin \theta_i \text{ Eqn (6)}$$

$$d_{0i} = 1 - d_{\alpha i} - d_{\beta i}$$

Where m_i is the modulation index of VSR and $0 \leq m_i < 1$.

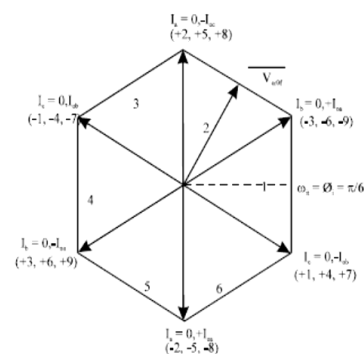


Fig-(2): Input Current vector

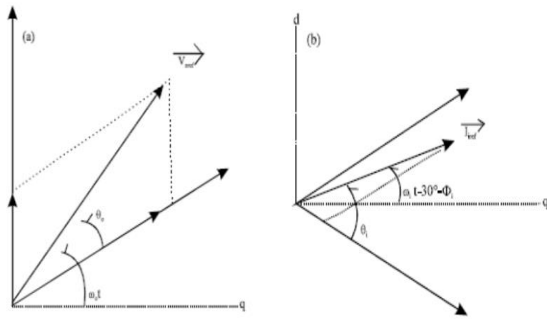


Fig-(3): Vector diagrams

Within first sector,

$$\begin{bmatrix} \tilde{i}_a \\ \tilde{i}_b \\ \tilde{i}_c \end{bmatrix} = \begin{bmatrix} d_{\alpha i} + d_{\beta i} \\ -d_{\alpha i} \\ -d_{\beta i} \end{bmatrix} \cdot I_{dc} \quad \text{.....Eqn (7)}$$

$$\theta_i = (\omega t - \phi_i) + \pi / 6, \pi / 6 \leq \omega t - \phi_i \leq -\pi / 6$$

The transfer matrix of VSR is,

$$\begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} = m_i \begin{bmatrix} \cos(\omega t - \phi_i) \\ \cos(\omega t - \phi_i - 2\pi / 3) \\ \cos(\omega t - \phi_i + 2\pi / 3) \end{bmatrix} I_{dc} \quad \text{.....Eqn (8)}$$

$$= T_{VSR} \cdot I_{dc} \quad \text{.....Eqn (9)}$$

Substituting the value of modulation index yields the desired input current phase. The VSR input voltage is given by,

$$\begin{aligned} \bar{V}_{pn} &= \bar{T}_{VSR}^T \cdot V_{iph} \\ &= \frac{3}{2} \cdot m_i \cdot V_{im} \cdot \cos \phi_i = \text{constant} \quad \text{.....Eqn (10)} \end{aligned}$$

b) VSI-SVM

Here only 6 switching combinations are allowed that yields non-zero output voltages. Hence the resulting output line space vector can assume the values from V₀-V₆ as shown in Fig-(4).

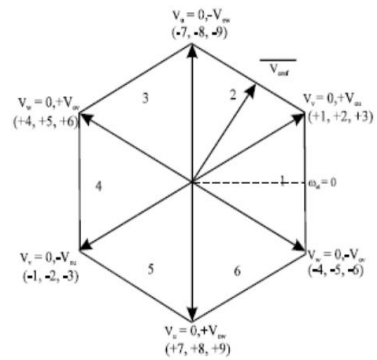


Fig-(4): Output voltage vector

The space vector of output line is defined as,

$$\begin{aligned} \bar{v}_o &= \sqrt{3} \cdot v_{oi} \cdot e^{j(\omega t - \phi + 30^\circ)} \\ v_{oi}; i \in \{u, v, w\} \end{aligned} \quad \text{.....Eqn (11)}$$

The duty cycle of switching state vectors are as stated below

$$\begin{aligned} d_{\alpha v} &= m_v * \sin(\pi / 3 - \theta_v) \\ d_{\beta v} &= m_v * \sin \theta_v \\ d_{0v} &= 1 - d_{\alpha v} - d_{\beta v} \end{aligned} \quad \text{.....Eqn (12)}$$

$$0 \leq m_v = \left(\frac{\sqrt{3} v_{oi}}{V_{dc}} \right) \leq 1 \quad \text{.....Eqn (13)}$$

Where m_v is the VSI modulation index.

The six vectors of the VSI corresponds to the 6 sextants of the three has output voltage as shown in Fig-(5).

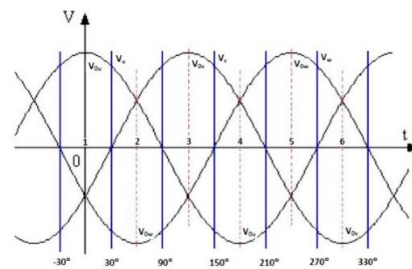


Fig-(5): Output line voltages of six sextants.

The averaged output voltages are,

$$\begin{bmatrix} v_a \\ v_b \\ v_c \end{bmatrix} = \begin{bmatrix} d_{\alpha v} + d_{\beta v} \\ -d_{\alpha v} \\ -d_{\beta v} \end{bmatrix} V_{dc} \quad \text{.....Eqn(14)}$$

$$= m_v * \begin{bmatrix} \cos(\theta_v - \pi / 6) \\ -\sin(\pi / 3 - \theta_v) \\ -\sin(\theta_v) \end{bmatrix} V_{dc} \dots\dots\dots\text{Eqn(15)}$$

For the first sextant,

$$-30^\circ \leq \omega_o t - \phi_o + 30^\circ \leq +30^\circ$$

$$\theta_v = (\omega_o t - \phi_o + 30^\circ) + 30^\circ \dots\dots\dots\text{Eqn(16)}$$

Substituting Eqn (16) in Eqn(14),

$$\begin{bmatrix} \bar{V}_u \\ \bar{V}_v \\ \bar{V}_w \end{bmatrix} = m_v * \begin{bmatrix} \cos(\omega_o t - \phi_o + 30^\circ) \\ \cos(\omega_o t - \phi_o + 30^\circ - 120^\circ) \\ \cos(\omega_o t - \phi_o + 30^\circ + 120^\circ) \end{bmatrix} * V_{dc} \dots\dots\dots\text{Eqn(17)}$$

Substituting for the modulation index we get,

$$V_o = \begin{bmatrix} \bar{V}_u \\ \bar{V}_v \\ \bar{V}_w \end{bmatrix} = \sqrt{3} V_{oi} * \begin{bmatrix} \cos(\omega_o t - \phi_o + 30^\circ) \\ \cos(\omega_o t - \phi_o + 30^\circ - 120^\circ) \\ \cos(\omega_o t - \phi_o + 30^\circ + 120^\circ) \end{bmatrix} \dots\dots\dots\text{Eqn(18)}$$

The averaged input current of VSI is,

$$\bar{i}_p = T_{VSI}^T \cdot i_o = \frac{\sqrt{3}}{2} I_{om} m_v \cos(\phi_L) = \text{constan } t \dots\dots\dots\text{Eqn(19)}$$

Since both VSI and VSR SVM contains six sectors each, there are a total of 36 combinations available. If the first voltage sector and the first current sector are active, the transfer matrix is,

$$\bar{T}_{ph} = m \begin{bmatrix} \cos(\theta_v - \pi / 6) \\ -\sin(\pi / 3 - \theta_v) \\ -\sin(\theta_v) \end{bmatrix} \begin{bmatrix} \cos(\theta_i - \pi / 6) \\ -\sin(\pi / 3 - \theta_i) \\ -\sin(\theta_i) \end{bmatrix}^T \dots\dots\dots\text{Eqn(20)}$$

$$V_o = \begin{bmatrix} \bar{V}_u \\ \bar{V}_v \\ \bar{V}_w \end{bmatrix} = \begin{bmatrix} d_{av} + d_{\beta v} \\ -d_{av} \\ -d_{\beta v} \end{bmatrix} * \begin{bmatrix} d_{ai} + d_{\beta i} \\ -d_{ai} \\ -d_{\beta i} \end{bmatrix}^T * \begin{bmatrix} v_a \\ v_b \\ v_c \end{bmatrix} \dots\dots\dots\text{Eqn(21)}$$

The output line voltages are,

$$V_o = \begin{bmatrix} \bar{V}_u \\ \bar{V}_v \\ \bar{V}_w \end{bmatrix} = \begin{bmatrix} d_{av} + d_{\beta v} \\ -d_{av} \\ -d_{\beta v} \end{bmatrix} * \begin{bmatrix} d_{ai} + d_{\beta i} \\ -d_{ai} \\ -d_{\beta i} \end{bmatrix}^T * \begin{bmatrix} v_a \\ v_b \\ v_c \end{bmatrix} \dots\dots\dots\text{Eqn(22)}$$

This yields to,

$$\begin{bmatrix} \bar{V}_u \\ \bar{V}_v \\ \bar{V}_w \end{bmatrix} = \begin{bmatrix} d_{\alpha vi} + d_{\beta vi} \\ -d_{\alpha vi} \\ -d_{\beta vi} \end{bmatrix} V_{ab} + \begin{bmatrix} d_{\alpha \beta vi} + d_{\beta vi} \\ -d_{\alpha \beta vi} \\ -d_{\beta vi} \end{bmatrix}^T V_{ac} \dots\dots\dots\text{Eqn(23)}$$

Where,

$$d_{\alpha_{vi}} = d_{av} d_{ai} = m \cdot \sin(\pi / 3 - \theta_v) \cdot \sin(\pi / 3 - \theta_i) = \frac{T_{\alpha_{vi}}}{T_s}$$

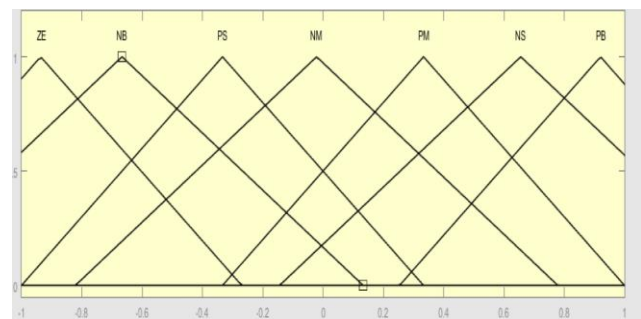
$$d_{\beta_{\alpha_{vi}}} = d_{\beta v} d_{ai} = m \cdot \sin(\theta_v) \cdot \sin(\pi / 3 - \theta_i) = \frac{T_{\beta_{\alpha_{vi}}}}{T_s}$$

$$d_{\alpha \beta_{vi}} = d_{ai} d_{\beta v} = m \cdot \sin(\pi / 3 - \theta_v) \cdot \sin(\theta_i) = \frac{T_{\alpha \beta_{vi}}}{T_s}$$

$$d_{\beta_{vi}} = d_{\beta i} d_{\beta v} = m \cdot \sin(\pi / 3 - \theta_v) \cdot \sin(\theta_i) = \frac{T_{\beta_{vi}}}{T_s} \dots\dots\dots\text{Eqn(24)}$$

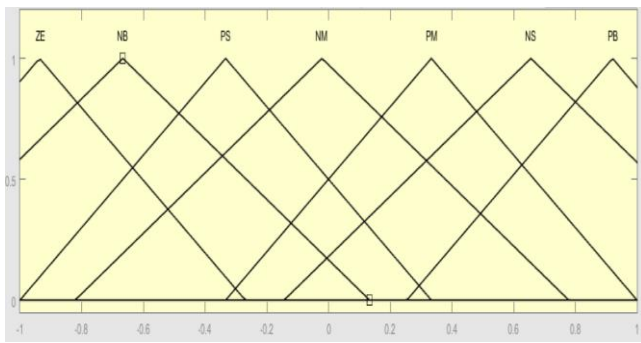
4. PI AND FUZZY LOGIC CONTROLLER

The currents of RL load are measured using PI controller. The Kp and Ki parameters were obtained by trial and error method. Fuzzy Logic Controller is gaining popularity because of its simple methodology. It is based on ‘if-then rules’. The inputs to the fuzzy controller are error and change in error. In an FLC each input and output variables are defined a membership function individually. A mamdani type fuzzy logic controller is used. For both the inputs triangular membership functions has been used. They have seven membership functions and they are shown in Fig-(6). The membership function of output signal of FLC is shown in Fig-(7).



(a)

Fig-(6): Membership functions (M.F) of FLC. (a) M.F of error.



(b)

Fig-(6): (b) M.F of change in error

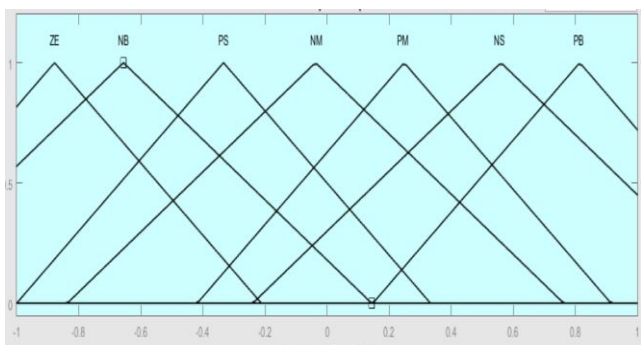


Fig-(7):M.F of output signal of FLC.

Table 2: The rules used in the FLC.

e/ce	NB	NM	NS	ZE	PS	PM	PB
NB	NB	NB	NB	NB	NM	NS	ZE
NM	NB	NB	NB	NM	NS	ZE	PS
NS	NB	NB	NM	NS	ZE	PS	PM
ZE	NB	NM	NS	ZE	PS	PM	PB
PS	NM	NS	ZE	PS	PM	PB	PB
PM	NS	ZE	PS	PM	PB	PS	PB
PB	ZE	PS	PM	PB	PB	PB	PB

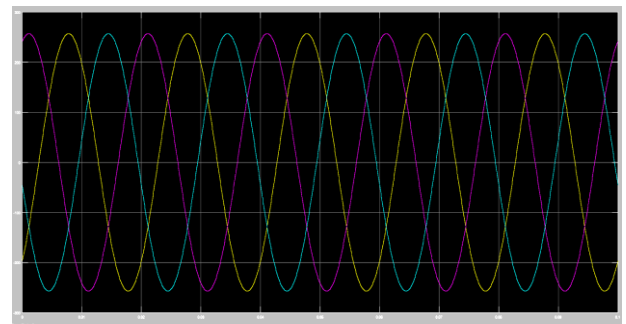
PB:PositiveBig;PM:PositiveMedium;PS:Positive Small;NS:NegativeSmall; NB:Negative Big; NM:Negative Medium;ZE:Zero;

5.SIMULATION RESULTS

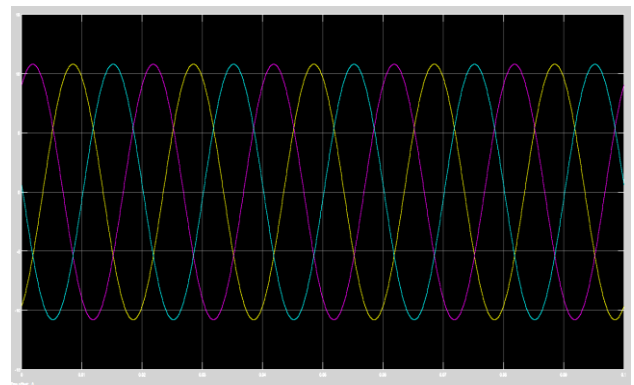
The PI and FLC are applied to the matrix converter and the simulation results are carried out using Matlab/Simulink/SimPowersystems. The system parameters are listed in appendix.

a) Balanced grid case

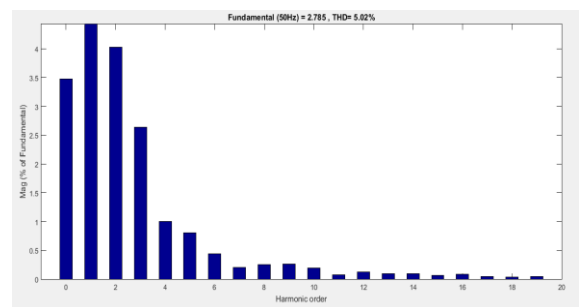
Here the supply input voltage in all the three phases is equal and there will be no distortions are shown in Fig-(8).



(a)



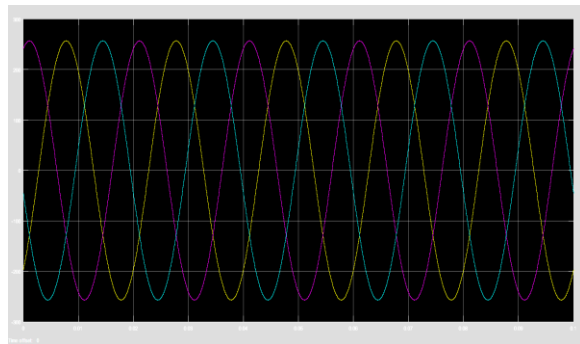
(b)



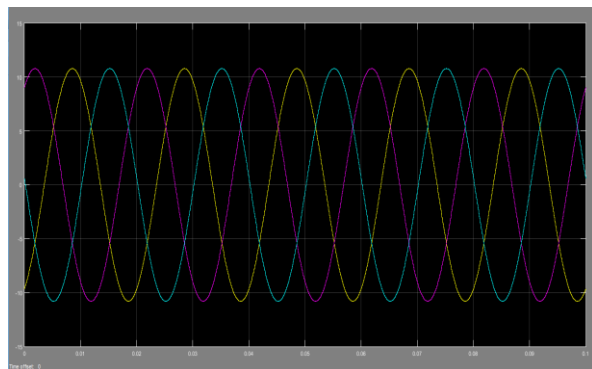
(c)

Fig- (8):(a) Output voltage.(b) Output current (c) Total Harmonic Distortion (THD) analysis of the MC with PI controller under balanced grid case

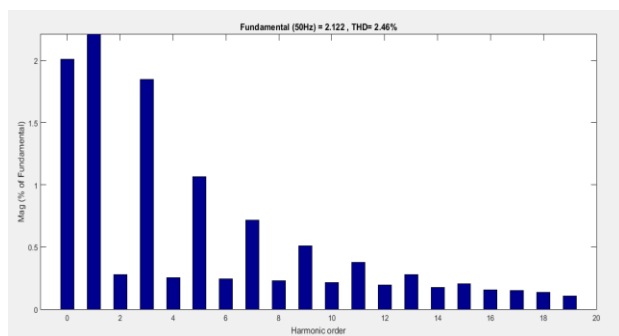
The output voltage and current waveform of the MC with FLC under balanced grid case is shown in Fig-(9).



(a)



(b)



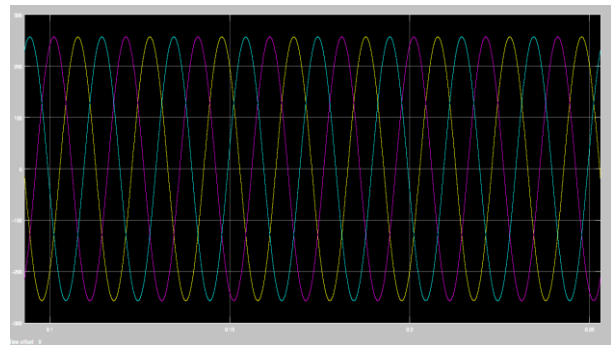
(c)

Fig-(9): (a) Output voltage (b) Current waveform(c) THD analysis of the MC with FLC under balanced grid case.

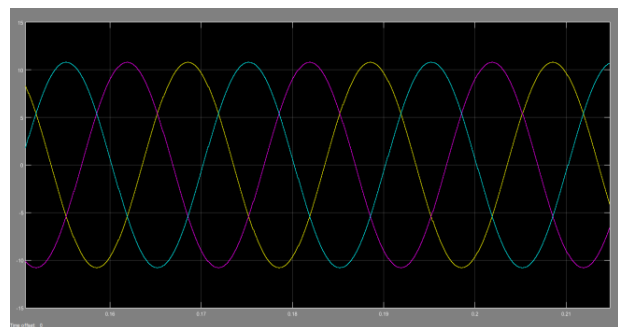
B)Unbalanced grid case

The performance of matrix converter under unbalanced condition has been analysed by introducing the distortions at the input supply.

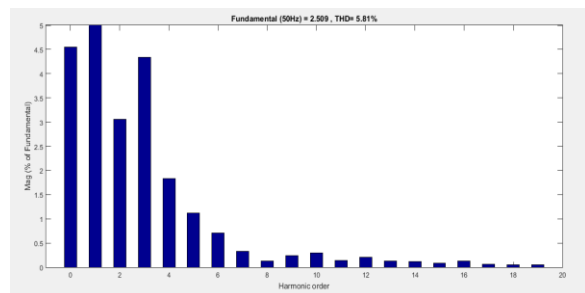
The output voltage and current waveform of the MC with PI controller under unbalanced grid case is shown in Fig-(10).



(a)



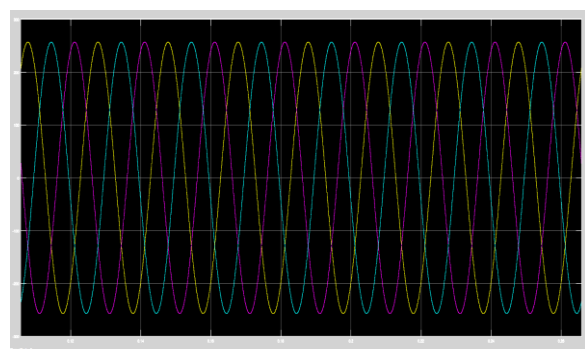
(b)



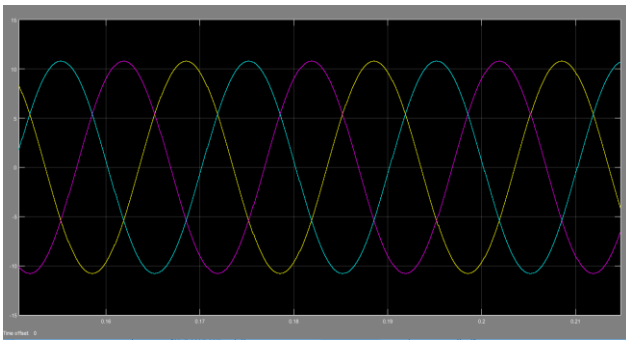
(c)

Fig- (10): (a) Output voltage (b) Current waveform (c) THD analysis of the MC with PI controller under unbalanced grid case.

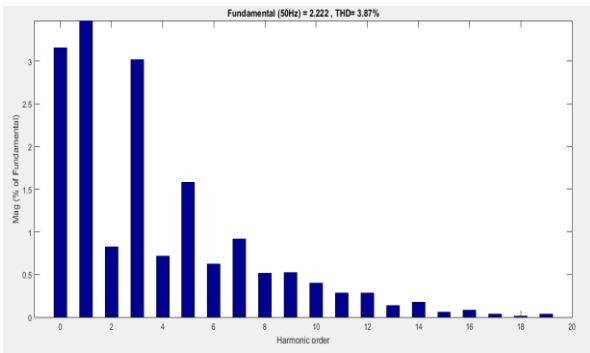
The output voltage and current waveform of the MC with FLC under unbalanced grid case is shown in Fig-(11).



(a)



(b)



(c)

Fig-11): (a) Output voltage (b) Current waveform (c) THD analysis of the MC with FLC under unbalanced grid case

Table-3: Comparison of THD

Sl No	Controller	THD
1	PI(Balanced grid case)	5.02%
2	FLC(balanced grid case)	2.46%
3	PI(unbalanced grid case)	5.81%
4	FLC(unbalanced grid case)	3.87%

From the table-3 , we can observe that the performance of the matrix converter has been improved with the use of Fuzzy controller as compared to the PI controller.

6.CONCLUSION

In this paper, both PI and FLC have been employed to control the matrix converter. FLC continuously adjusts the load current by reducing the error. From the comparison of the THD in all the four cases, it can be concluded that the performance of FLC is good in contrast to that of PI controller.

REFERENCES

- [1] Saul Lopez Arevalo, “Matrix converter for frequency changing power supply applications”, 2008.
- [2] Alesina and M.Venturini , “Analysis and design of optimum amplitude nine switched direct AC-AC converters”,IEEE Transactions on Power Electronics,vol.4,no.1,pp.101-112,Jan 1989.
- [3] L.Huber and D.Borojevic, “Space vector modulated three phase to three phase matrix converter with input power factor correction”, IEEE Transactions on Power Electronics,vol.31,no.6,pp.1234-1246,Nov/Dec1995.
- [4] Casadei, G.Serra and A.Tani, “Reduction of the input current harmonic content in matrix converters under input / output unbalance”, IEEE Transactions on Industrial Electronics,vol.5,no.3,pp.401-411,Jun 1998.
- [5] Patrick W Wheeler , “Matrix converter: A technological Review”, IEEE Transactions on Industrial Electronics,vol.49,no.2,pp.276-286,April 2002.
- [6] L.Zhang, C.Watthanasarn, W.Shepherd, “Analysis and comparison of control technology for AC-AC matrix converter”, IEE proc-Electr.Power Appl,vol.145,no.4,July 1998.
- [7] L.Zarri,M.Mengoni and O.Ojo, “Improvement in the control range of matrix converter”, in proc.IEEE ECCE’11, Phoenix,AZ,Sept.2011.
- [8] Dr. Sarat Kumar Sahoo, K.V. Kandasamy, Sruty Shankar Singh, “Three-Phase Matrix Converter with Ripple Free Output” Third International Conference on Sustainable Energy and Intelligent System (seiscon 2012),VCTW, Tiruchengode, Tamilnadu, India on 27-29 December, 2012.
- [9] A. Boukadoum, T. Bahi, S. Oudina, Y. souf, S. Lekhchine, “Fuzzy control adaptive of a matrix converter for harmonic compensation caused by nonlinear loads”, Energy Procedia 18 (2012) 715 – 723.
- [10] B.Hamane M. L. Doumbia A. Cheriti K. Belmokhtar, “Comparative Analysis of PI and Fuzzy Logic Controllers for Matrix Converter”, 2014 Ninth International Conference on Ecological Vehicles and Renewable Energies (EVER).