

# Selective Harmonics Elimination for Five Level Inverters with Unequal DC Sources

Miss. Mitalee Anil Kamble <sup>1</sup>, Prof. K. Chandra Obula Reddy<sup>2</sup>

<sup>1</sup>Student, Matsyodari Shikshan Sanstha's, College Of Engineering and Technology Jalana-431203 (MS) India

<sup>2</sup>Assistant Professor, Matsyodari Shikshan Sanstha's, College Of Engineering and Technology, Jalana-431203 (MS)

**Abstract** - In virtue of the use of a set of independent dc sources, multilevel inverters (MLI) are capable to operate at higher voltage levels and at lower switching frequencies than conventional inverters. In order to limit losses and to increase converter efficiency, very low frequency modulation techniques should be adopted, such as selective harmonic elimination (SHE) at fundamental frequency operation. This paper proposes a modulation technique based on a harmonic elimination method developed for five level inverters with unequal dc link voltages. The approach is based on the analytical solution of nonlinear equations associated with the inverter. After a description of the method, an analysis on the computed switching angles and on their impact on total harmonic distortion is reported with the support of simulation result.

**Key Words:** Selective Harmonic Elimination (SHE), 5-level cascaded inverter, Unequal dc sources, Total Harmonic Distortion (THD).

## 1. INTRODUCTION

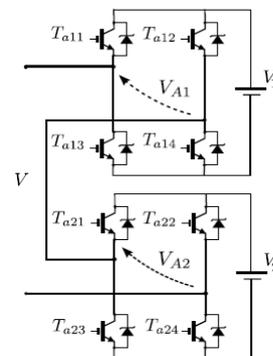
In firmness of purpose for the use of independent DC sources, multilevel inverters (MLI), can be capable to operate at higher voltage levels and at lower switching frequencies than conventional inverters and similarly, to synthesize staircase output voltage waveforms ensuring better harmonic content and reduced electromagnetic interferences (EMI). For such reasons, nowadays, they are considered a very appealing solution for medium and high voltage power applications [1]. Very low frequency modulation techniques should be adopted. In order to limit losses and to increase converter efficiency, one of which is selective harmonic elimination (SHE). In [2] the theory of symmetric polynomials has been applied. By increasing the number of levels, the polynomials degree tends to be high; hence the mathematical complexity of the SHE problem reaches a high level. The non-linear equations describing the problem can be solved using some iterative techniques such as the Newton-Raphson method, but they need a proper initial guess. Heuristic algorithms, such as genetic algorithm (GA) and particle swarm optimization (PSO) are very interesting because, for the range space where there is no analytical solution, they will find the nearest solution

providing a smooth data set [3]. However, they present the disadvantage of computationally intensive and time-consuming equations. Therefore, switching angles are calculated offline. In [4] the Bee algorithm (BA) is applied to a 5-level inverter for solving the equations and the superiority.

## 2. Analytical Procedure for Switching Angle Computation

A five-level cascaded inverter fed by two separate unequal dc sources  $V_1$  and  $V_2$  is shown in Fig. 1. For such an inverter the switching angles  $\alpha_1$  and  $\alpha_2$ .  $0 < \alpha_1 < \alpha_2 < \pi$ . that eliminates a harmonic from the output voltage waveform can solving the following system:

$$\begin{aligned} u_1 \cos(k\alpha_1) + u_2 \cos(k\alpha_2) &= 0 \\ u_1 \cos(\alpha_1) + u_2 \cos(\alpha_2) - m_1 &= 0 \dots\dots(1) \end{aligned}$$



**Fig.1:** Single-phase cascaded H-Bridge five level inverter with unequal dc sources.

Where,  $k = 3, 5, 7$  and  $m_1$  is modulation index.

### a) By Eliminating 3<sup>rd</sup> Harmonics :

By introducing Chebyshev polynomials:

$$T_1(x_i) = x_i = \cos(\alpha_i) \tag{2}$$

$$\text{And } T_3(x_i) = 4x_i^3 - 3x_i = \cos(3\alpha_i) \tag{3}$$

$i = 1, 2$  by transforming polynomial system

$$\begin{cases} v_1 T_3(x_1) + v_2 T_3(x_2) = 0 \\ v_1 T_1(x_1) + v_2 T_1(x_2) - m_1 = 0. \end{cases} \tag{4}$$

The following final system can be obtained:  
 $a_3 x^3 + a_2 x^2 + a_1 x + a_0 = 0$

$$x_2 = -b_1x_1 + b_0 \tag{5}$$

The solutions of the system (5) are three, belonging to C2 but only one solution (x1, x2) is valid because it has to satisfy the following two conditions:

- Each component of the solution should belong to the interval [0, 1], and
- $x_1 > x_2$ .

The switching angles are computed as  $\alpha_1 = \arccos(x_1)$  and  $\alpha_2 = \arccos(x_2)$  fig 2 shows curve for  $k=3$ . With two functions denoted by dash curve and continuous line.

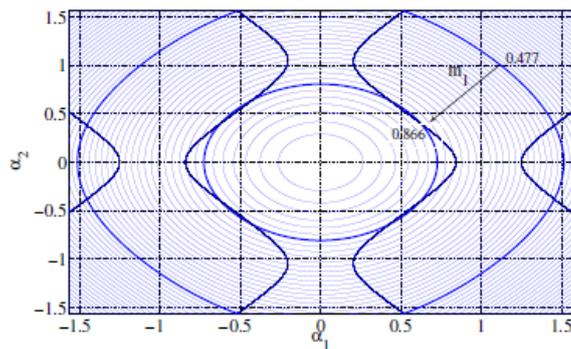


Fig 2: Curves in implicit form of the system (1) for  $k = 3$ .

**b) By Eliminating 5<sup>th</sup> Harmonics:**

By considering the term,

$T_5(x_i) = 16x_i^5 - 20x_i^3 + 5x_i = \cos(5\alpha_i)$   $i = 1, 2$ , in addition to  $T_1(x_i)$  and  $T_3(x_i)$ , the following polynomial system can be obtained:

$$\begin{aligned} v_1T_5(x_1) + v_2T_5(x_2) &= 0 \\ v_1T_1(x_1) + v_2T_1(x_2) - m_1 &= 0 \end{aligned} \tag{6}$$

Algebraic manipulations lead to the following system:

$$\begin{aligned} c_5x^5 + c_4x^4 + c_3x^3 + c_2x^2 + c_1x + c_0 &= 0 \\ x_2 &= -b_1x_1 + b_0 \end{aligned} \tag{7}$$

Where,  $c_5 = 16v_1(1-b_1^4)$ ,  $c_4 = 80m_1b_1^4$ ,  $c_3 = 20v_1[b_1^2(1-8b_2^0) - 1]$ ,  $c_2 = 20m_1b_2^1(8b_2^0 - 3)$ ,  $c_1 = 20m_1b_0b_1(3 - 4b_2^0)$ ,  $c_0 = m_1[5 + 4b_2^0(4b_2^0 - 5)]$ , Among the five solutions of the system (7) in C2 two at most valid. Figure 3 shows, for  $k = 5$ ,

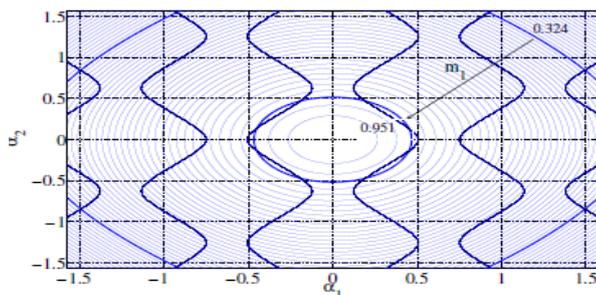


Fig 3: Curves in implicit form of the system (1) for  $k = 5$ .

### 3. Application

A five-level cascaded inverter characterized by  $v_1 = 0.55$  and  $v_2 = 0.45$  has been considered and the proposed procedure has been applied for computation of the switching angles in two distinct cases:

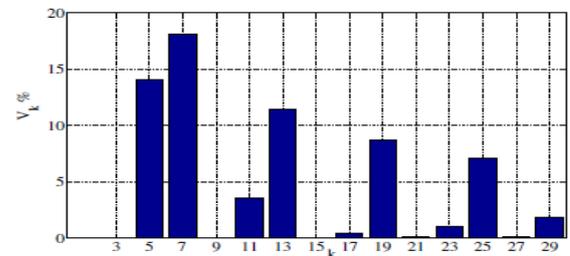


Fig 4:  $V_k \%$  obtained eliminating the third harmonic for  $m_1 = 0.5$  and  $\alpha_1 = 0.49465$  rad,  $\alpha_2 = 1.53540$  rad.

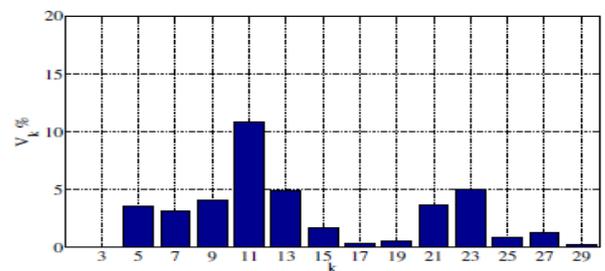


Fig 5:  $V_k \%$  obtained eliminating the third harmonic for  $m_1 = 0.83$  for  $\alpha_1 = 0.28436$  rad,  $\alpha_2 = 0.83483$  rad

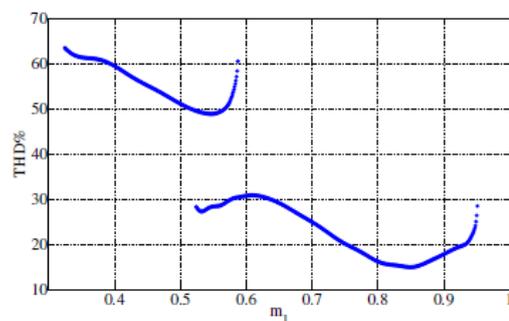


Fig 6: THD% obtained eliminating the fifth harmonic.

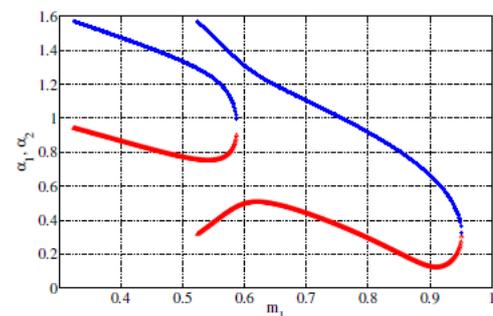


Fig 7: Computed switching angles for fifth harmonic elimination.

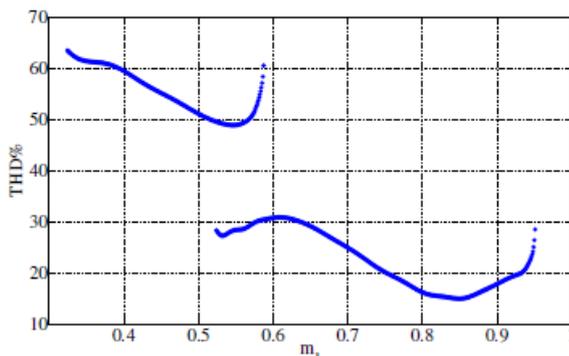


Fig 8: THD% obtained eliminating the fifth harmonic.

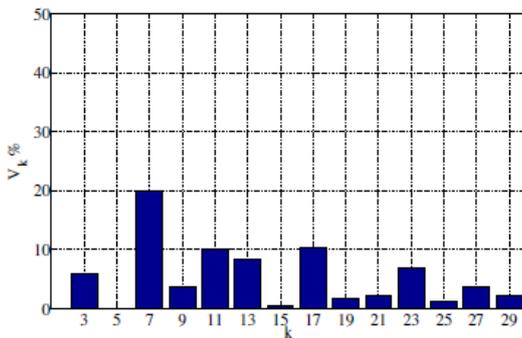


Fig 9: % obtained eliminating the fifth harmonic for  $m1 = 0.55$ ,  $\alpha1 = 0.38753$  rad,  $\alpha2 = 1.48004$  rad

Considering the unbalanced cases, for third harmonic elimination there are two intervals of the modulation index, in  $[0; 1]$ , where the solution exists. They become smaller if  $v2$  grows. In this case, the first interval moves to left. For fifth harmonic elimination, for unbalancing, there are three intervals of the modulation index in  $[0; 1]$ , where one or two solutions do exist. Also in this case, these intervals become smaller when  $v2$  grows. As shown in Fig. 20, for modulation index values variable between about 0.35 and 0.5, the unbalancing gives better THD values.

#### 4. Conclusion

In this paper a modulation technique based on the selective harmonic elimination method applied to multilevel converters with unequal dc link voltages has been presented. The analytical solution of the nonlinear equations corresponding to the mathematical model of the problem has been obtained in terms of switching angles and THD%. Unbalancing, characterized by  $v1 < v2$  and by  $v2 < v1$  have been considered for either third or difference between  $v1$  and  $v2$  increases.

#### References

[1] S. Kouro, M. Malinowski, K. Gopakumar, J. Pou, L. G. Franquelo, B. Wu, J. Rodriguez, M. A. Perez, and J. I. Leon, (2010) "Recent advances and industrial applications of multilevel converters," IEEE Trans. Ind. Electron., vol. 57, no. 8, pp. 2553-2580,

[2] J. N. Chiasson, L. M. Tolbert, K. J. McKenzie, and Z. Du, "A unified approach to solving the harmonic elimination equations in multilevel converters (2004)" IEEE Trans. Power Electron., vol. 19, no. 2, pp. 478- 490,

[3] F. Filho, H.Z. Maia, T.H.A. Mateus, B. Ozpineci, L.M. Tolbert and J.O.P. Pinto (2013) "Adaptive selective harmonic minimization based on ANNs for cascade multilevel inverters with varying dc sources," IEEE Trans. Ind. Electron., vol. 60, no. 5, pp. 1955-1962,

[4] A. Kavousi, B. Vahidi, R. Salehi, M.K. Bakhshizadeh, N. Farokhnia, S. Hamid Fathi, (2012) "Application of the Bee algorithm for selective harmonic elimination strategy in multilevel inverters," IEEE Trans. Power Electron., vol. 27, no. 4, pp. 1689 - 1696,

[5] B. Diong, H. Sepahvand, K. A. Corzine (2013) "Harmonic Distortion Optimization of Cascaded H-Bridge Inverters Considering Device Voltage Drops Noninteger DC Voltage Ratios," IEEE Trans. Ind. Electron., vol. 60, no. 8, pp. 3106-3114,.

[6] H. Sepahvand, J. Liao, and M. Ferdowsi (2011) "Investigation on capacitor voltage regulation in cascaded H-bridge multilevel converters with fundamental frequency switching," IEEE Trans. Ind. Electron., vol. 58, no. 11, pp. 5102-5111,

[7] M. S. A. Dahidah and V. G. Agelidis (2008) "Selective harmonic elimination PWM control for cascaded multilevel voltage source converters: A generalized formula," IEEE Trans. Power Electron., vol. 23, no. 4, pp. 1620-1630, [8] L. M. Tolbert, J. N. Chiasson, Z. Du, and K. J. McKenzie (2005) "Elimination of harmonics in a multilevel converter with nonequal DC sources," IEEE Trans. Ind. Appl., vol. 41, no. 1, pp. 75-82,