SIMULATION OF DSTATCOM FOR VOLTAGE CONTROL AND POWER PROFILE IMPROVEMENT

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Abstract - This paper present enhancement of the voltage sag, harmonic distortion and low power factor using DSTATCOM with LCL passive filter in distribution system. This model is based on VSI statcom inject the compensating current to the system to mitigate the voltage sag and lcl filter was added to reduce the harmonic distortion. By using phase shift and space vector pulse width modulation technique (svpwm) technique voltage sag can be reduced.

Key Word: voltage source inverter, Voltage sag, Dstatcom, phase shift control, svpwm, PI controller.

I INTRODUCTION

An increasing demand for high quality, reliable electrical power and increasing number of distorting loads may leads to an increased awareness of power quality both by customers and utilities. The most common power quality problems today are voltage sags, harmonic distortion and low power factor.

Voltage sags is a short time (10 ms to 1 minute) event during which a reduction in r.m.s voltage magnitude occurs [4]. It is often set only by two parameters, depth/magnitude and duration. The voltage sags magnitude is ranged from 10% to 90% of nominal voltage and with duration from half a cycle to 1 min. Voltage sags is caused by a fault in the utility system, a fault within the customer’s facility or a large increase of the load current, like starting a motor or transformer energizing [2,3]. Voltage sags are one of the most occurring power quality problems. For an industry voltage sags occur more often and cause severe problems and economical losses. Utilities often focus on disturbances from end-user equipment as the main power quality problems [5]. Harmonic currents in distribution system can cause harmonic distortion, low power factor and additional losses as well as heating in the electrical equipment. It also can cause vibration and noise in machines and malfunction of the sensitive equipment. The development of power electronics devices such as Flexible AC Transmission System (FACTS) and customs power devices have introduced and emerging branch of technology providing the power system with versatile new control capabilities [1]. There are different ways to enhance power quality problems in transmission and distribution systems. Among these, the DSTATCOM is one of the most effective devices. A new PWM-based control scheme has been implemented to control the electronic valves in the DSTATCOM. The D-STATCOM has additional capability to sustain reactive current at low voltage, and can be developed as a voltage and frequency support by replacing capacitor with batteries as energy storage [6, 7].

In this paper, the configuration and design of the DSTATCOM with LCL Passive Filter are analyzed. It is connected in shunt or parallel to the 11 kV test distribution system. It also is design to enhance the power quality such as voltage sags, harmonic distortion and low power factor in distribution system.

II SYSTEM CONFIGURATION

Fig. 1 shows the basic circuit diagram of a DSTATCOM system with lagging power-factor loads connected to a three phase three-wire distribution system. Lagging power-factor load is realized by star-connected resistive-inductive (R-L) load. An unbalanced load is realized by disconnecting load from phase a using a circuit breaker. A three-phase voltage source converter (VSC) working as a DSTATCOM is realized using six insulated-gate bipolar transistors (IGBTs) with antiparallel diodes. At ac side, the interfacing inductors are used to filter high-frequency components of compensating currents.
A. Voltage Source Converter (VSC)

A voltage-source converter is a power electronic device that connected in shunt or parallel to the system. It can generate a sinusoidal voltage with any required magnitude, frequency and phase angle. The VSC used to either completely replace the voltage or to inject the 'missing voltage'. The 'missing voltage' is the difference between the Nominal voltage and the actual. It also converts the DC voltage across storage devices into a set of three phase AC output voltages \[8, 9\].

In addition, D-STATCOM is also capable to generate or absorbs reactive power. If the output voltage of the VSC is greater than AC bus terminal voltages, D-STATCOM is said to be in capacitive mode. So, it will compensate the reactive power through AC system and regulates missing voltages. These voltages are in phase and coupled with the AC system through the reactance of coupling transformers. Suitable adjustment of the phase and magnitude of the DSTATCOM output voltages allows effective control of active and reactive power exchanges between D-STATCOM and AC System. In addition, the converter is normally based on some kind of energy storage, which will supply the converter with a DC voltage \[10\].

B. Controller

The principle drift of the control method is to preserve continuous voltage magnitude at the place where a sensitive load is coupled, beneath system cohibitions. RMS voltage at the load point is measured in this control technique, i.e. there is no.

Requirement of reactive power measurements. for switching of VSC sinusoidal PWM scheme is utilized as sine PWM bears simple and quick reflection compared to other methods of PWM such as space vector PWM.

From the RMS value of reference voltage and RMS value of terminal voltage an error signal is generated which is fed to the controller. This error signal is processed by PI controller and output angle \(\delta\) is generated, which is applied to the PWM signal generator. Phase angle of the balanced supply voltages which is assumed to be 120° summed with the angle of the output of PI to produce the desired synchronizing signal required to operate the PWM generator. Here in converter, there is both active and reactive power exchange takes place simultaneously. Now the error signal is obtained by comparison of the reference voltage with the RMS voltage measured at the load point. This error signal is processed by the PI controller which in return generates the required angle to drive the error to zero, i.e., the load RMS voltage is brought back to the reference voltage.

D. LCL Passive Filter

LCL Passive filter is more effective on reducing harmonic distortion. To design it, equation (2.3), (2.4) and (2.5) are used.

\[
L_g = \frac{E_g}{2\sqrt{3}i_{\text{rms}} f_{sw}}
\]

\[
L_c = \frac{L_g}{2}
\]

\[
C_f = \frac{L + L_g}{LL_g (2\pi f_{sw})^2}
\]

To design an efficient LCL Passive filters make sure that
C. Simulink Model for the test system

The test system was designed using MATLAB Simulink as shown in the figure below.

To create distortion in the distribution system, different types of fault such as Three Phase to Ground (TPG), Double Line to Ground (DLG), Line to Line (LL), and Single Line to Ground (SLG) are injected.

1. Without insertion of D-STATCOM

| Table. Results of Voltage Sags for Different Types of Fault |
|-----------------|-----------------|-----------------|-----------------|-----------------|
| Fault Resistance Rf, Ω | Voltage Sag For SLG Fault (p.u)| Voltage Sag For DLG Fault (p.u) | Voltage Sag For TPG Fault (p.u) | Voltage Sag For LL Fault (p.u) |
| 0.66 | 0.7005 | 0.6257 | 0.5977 | 0.6578 |

Table shows the overall results of voltage sags in p.u for different types of fault. From the table, it can be observed that when the value of fault resistance is increased, the voltage sags will also increase for different types of fault.

A. Without insertion of D-STATCOM

[Fig. Voltage sag is 0.7005 p.u during SLG fault without D-STATCOM]

[Fig. Voltage sag is 0.6257 p.u during DLG fault without D-STATCOM]

[Fig. Voltage sag is 0.5977 p.u during TPG fault without D-STATCOM]
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<table>
<thead>
<tr>
<th>Fault Resistance $R_f$, Ω</th>
<th>Voltage Sag For SLG Fault (p.u)</th>
<th>Voltage Sag For DLG Fault (p.u)</th>
<th>Voltage Sag For TPG Fault (p.u)</th>
<th>Voltage Sag For LL Fault (p.u)</th>
</tr>
</thead>
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<tr>
<td>0.66</td>
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<td>0.6257</td>
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<tr>
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<td>0.6809</td>
<td>0.6623</td>
<td>0.7026</td>
</tr>
</tbody>
</table>

B. With Insertion of DSTATCOM

Voltage sag is 0.9342 p.u during SLG Fault with DSTATCOM

Voltage sag is 0.9308 p.u during DLG Fault with DSTATCOM

Figure. Waveform of distortion output current without LCL Passive Filter

Figure shows the waveform of distortion output current and figure shows the spectrum of distortion output current.

Figure. Harmonic spectrum of distortion output current without LCL Passive Filter

Fundamental (50 Hz) = 0.7077, THD = 38.75%
VIII CONCLUSION

The simulation results show that the voltage sags can be mitigate by inserting D-STATCOM to the distribution system. By adding LCL Passive filter to D-STATCOM, the THD reduced within the IEEE STD 519-1992. The power factors also increase close to unity. Thus, it can be concluded that by adding D-STATCOM with LCL filter the power quality is improved.

VI. REFERENCES