A Low Noise Op-Amp Transimpedance Amplifier for InGaAs Photodetectors

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Abstract - The front-end design of the optical receiver contains a photodiode and transimpedance amplifier (TIA). It is a challenge to design an efficient transimpedance amplifier for optical receivers. TIA is used to convert low current signals (pA to μA) into a proportional output voltage. Op-amp based TIA is used in several applications because the op-amps are an attractive option for the TIA gain element due to a range of wideband low noise devices are commercially available and likewise it is relatively easy to modify a design for particular requirements. In this paper, we purpose the modeling, the design, and the development of a low noise op-amp transimpedance amplifier for InGaAs photodetectors in order to record low level optical (∼10 μW) signals.

Key Words: Fiber Bragg Grating, Operational Amplifier, Photodiodes, and Transimpedance Amplifier.

1. INTRODUCTION
Photodiodes and InGaAs Photodiode convert optical signals into current signal. These signals are usually small in magnitude (nA to μA in LIDAR applications [1], FBG sensor applications [2]) and are commonly amplified by a transimpedance amplifier (TIA) which converts the photocurrent into a proportional output voltage simultaneously amplifying the signal. It is also known as I-to-V converter [3].

An active transimpedance amplifier is based on active elements like BJTs, FETs, and operational amplifier (Op-amp). Transimpedance amplifier using op-amp is the widely used one. Fig.1 shows the active op-amp based transimpedance amplifier circuit with photodiode.

In the circuit shown in Fig.1, +V and –V are the supply voltages for the op-amp. $R_F$ and $C_F$ (necessary to maintain stability) are the feedback resistor and feedback capacitor respectively. $I_S$ is the photodiode current. The photodiode is connected between ground and the inverting input of the op-amp. The other input of the op-amp is connected to ground.

### 1.1 Design Requirements
The design requirements for the TIA are:

- Transimpedance gain of at least 10000 V/A
- -3 dB bandwidth of at least 5 MHz
- DC coupling of the photodiode to the TIA
- Guaranteed stability with a photodiode capacitance of up to 10 pF

2. DESIGN METHODOLOGY
Methodology adopted in this paper includes selection of various performance parameters for the efficient designing for TIA and selection of suitable op-amp considering the performance parameters.

2.1 Selection of various performance parameters for the efficient designing of TIA

**Gain:** The feedback resistor ($R_F$) value is used to define the gain of the TIA. For gain to be very high, feedback resistor is made as large as other constraints permit. The value of the feedback resistor is [4]:

$$ R_F = \frac{V_{out (max)} - V_{out (min)}}{I_{IN (max)}} \quad \text{... (1)} $$
where $V_{\text{OUT (MAX)}}$ = maximum output voltage; $V_{\text{OUT (MIN)}}$ = minimum output voltage; $I_{\text{IN (MAX)}}$ = maximum input current of the photodiode.

**Bandwidth:** The -3 dB bandwidth of the TIA is inversely proportional to the feedback resistor. Therefore, if the bandwidth is important then, small feedback resistor is used.

**Stability:** To stabilize the transimpedance amplifier, a large enough capacitor must be placed in parallel with the feedback resistor. The calculated value of feedback capacitor is [5]:

$$C_F = \frac{C_T}{\sqrt{2\pi f_{\text{GBW}} R_F}}$$

where $C_T$ = total capacitance of the photodiode and op-amp; $f_{\text{GBW}}$ = gain-bandwidth product of the op-amp and $R_F$ is the feedback resistor.

**Noise:** It is essential to take into account various noise sources (internal and external). Internal noise sources within the op-amp and external noise which the TIA may pick up such as power supply noise.

### 2.2 Selection of suitable op-amp considering the performance parameters

First, calculate the required op-amp gain-bandwidth product, i.e. [4]

$$f_{\text{GBW}} > \frac{C_T + C_F}{2\pi R_F f_{\text{GBW}}}$$

Also Op-amp with JFET or CMOS inputs are the most commonly selected type because these op-amps typically have much lower bias current than those with BJT input devices. This results in reduced DC error voltages and lower noise due to reduced input current noise.

The OPA656 IC (Texas Instruments), is selected for this design because of its excellent combination of low bias current, offset voltage, power consumption and wide bandwidth.

### 3. SIMULATION

The implementation of TIA coupled with the photodiode characteristics using NI Multisim Software. It includes the following steps:

- **Step 1:** Firstly examines and draw the equivalent circuit of the photodiode which contains the $C_S$ capacitance of photodiode, $R_{sh}$: Shunt resistance of the photodiode, $I_{S}$: photodiode reverse light current, and reverse bias voltage.
- **Step 2:** Calculate the total capacitance($C_T$) including the op-amp input capacitance ($C_i$) and photodiode capacitance ($C_S$).
- **Step 3:** Determine the magnitude of the possible reverse light current output from the photodiode, including the minimum current anticipated and maximum current anticipated.
- **Step 4:** Choose a feedback resistor value such that the input current levels create the desired output signal voltages, and ensures that the output voltages can accommodate the dynamic range of the input signal. The value of the feedback resistor can be calculated by dividing the maximum output voltage by the maximum input current:

$$R_F = \frac{V_{\text{OUT (MAX)}} - V_{\text{OUT (MIN)}}}{I_{\text{IN (MAX)}}}$$

- **Step 5:** Calculate the optimum feedback capacitance $C_F$ which is given as:

$$C_F = \frac{C_T}{\sqrt{2\pi R_F f_{\text{GBW}}}}$$

- **Step 6:** Calculate the bandwidth given the resulting component values.
- **Step 7:** Evaluate the circuit to determine if all design goals are satisfied and also conduct different types of simulations to determine the different properties of the circuit.

### 3.1 Simulation Types

We will conduct three different types of simulations to determine the different properties of our TIA. First, we make an AC analysis to determine the frequency response and gain. The result of the AC analysis is displayed in two parts: gain versus frequency and phase versus frequency. Next we perform the transient analysis, after satisfying ourselves with the AC analysis. In transient analysis, also called time-domain transient analysis, Multisim computes the circuit’s response as a function of time. The result of the transient analysis is a calculation of voltage versus time. The last one is noise analysis. It performs an AC analysis to determine the noise. Noise analysis produces an output noise spectrum, an input noise spectrum and, optionally, a component contribution spectrum. When the
analysis is finished, its results are displayed as a graph of voltage squared, $V^2$ versus frequency, Hz [6, 7].

### 3.2 Transimpedance Amplifier with OPA656 in Multisim

We have implemented the circuit shown in Figure 2 and have used the following values as our baseline values for simulations i.e., photodiode parameters.

\[ I_S = 10 \mu A; \quad R_{sh} = 1 G\Omega; \quad C_S = 0.3 \ pF \]

![Figure 2: Transimpedance amplifier, with OPA656 drawn in Multisim; Reverse light current $I_S=10 \mu A$ and the feedback resistance $R_{F}=100k\Omega$](image)

A high gain is first selected and the calculated $R_F$ (Equation 1) is $100 \ k\Omega$. Further, the photodiode capacitance is $C_S = 0.3 \ pF$ and the op-amp input capacitances are $C_{cm} = 0.7 \ pF$ and $C_{diff} = 2.8 \ pF$. Total capacitance is the sum of photodiode and op-amp capacitance. Therefore, $C_F = 3.5 \ pF$. Using the formula for $C_F$ (Equation 2) gives $C_F = 0.1 \ pF$. This measured value for $C_F$ gives too much ringing effect in the frequency response curve. Some overcompensation can be used to reduce the ringing. So, the value for $C_F$ is finally considered to be $0.2 \ pF$.

### 4. RESULTS AND DISCUSSION

#### 4.1 AC Analysis (Gain and Bandwidth)

The simulation results are shown in Figure 3. The top graph shows the gain of the TIA in decibels as a function of frequency of the input signal. The -3 dB bandwidth and the gain at -3 dB point are calculated to be 9.61 MHz and 97.02 dB respectively. The bottom graph shows the phase shift as a function of frequency. At -3 dB point, the phase drops to only 117 degrees from 180 degrees and the circuit remains stable.

![Figure 3: AC analysis (a) gain v/s frequency and (b) phase v/s frequency](image)

#### 4.2 Transient Analysis

As we have decided on OPA656 as our preferred op-amp for TIA, we look closely at some of the circuit characteristics. We use the transient analysis to see how the circuit reacts to a specified input signal, and to observe the shape of our output signal.

In Figure 4, we can see a typical input response of the transient analysis with OPA656. Our input signal for this analysis is a current signal which has a peak-to-peak value of 20 μA with a frequency of 1 kHz.

![Figure 4: Input signal from transient analysis with OPA656](image)

Figure 5 shows the output response of the transient analysis with OPA656. Our output signal for this analysis is a voltage signal which has a peak-to-peak value of 2V with a frequency of 1 kHz. As we are using an inverted
configuration, we see that $I_{in}$ and $V_{out}$ are inverted, as expected.

**Figure 5:** Output signal from transient analysis with OPA656

### 4.3 Noise Analysis

Now, we perform the simulation in Multisim, to view the noise traces, as follows in Figure 6. The Figure 6 shows that the noise voltage is constant for lower frequencies. For higher frequencies the input noise voltage increases, the output noise voltage drops considerably with frequency increasing.

**Figure 6:** Noise power spectral density curves

### 5. Conclusion

We have designed and simulated a low noise op-amp transimpedance amplifier for InGaAs photodetectors. We have simulated TIA circuit with NI Multisim (12.0.1) software. With low noise transimpedance amplifier, a bandwidth of $\sim 9.61$ MHz and a gain of $\sim 99.99$ kΩ have been achieved.

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### REFERENCES


BIOGRAPHIES

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